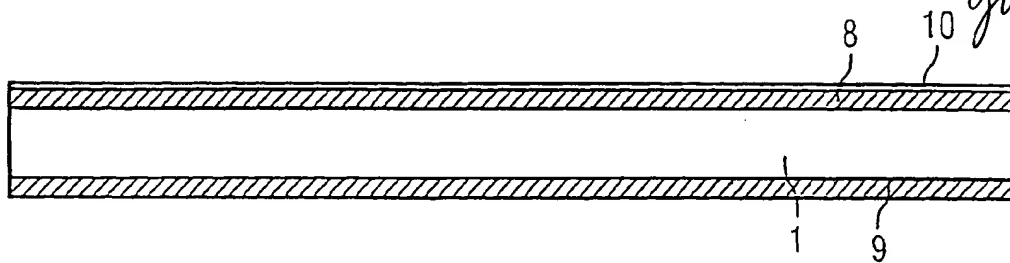
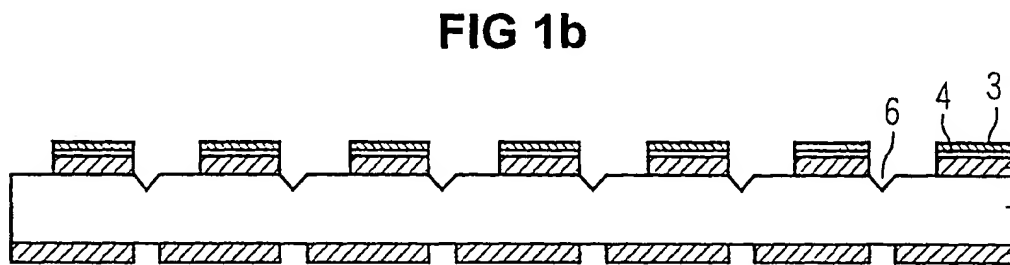


FIG 1a



*gold coating*

FIG 1b



*ports*  
*chip mounting area*  
*ceramic carrier*  
*cooling element*  
*metal surface*

FIG 1c

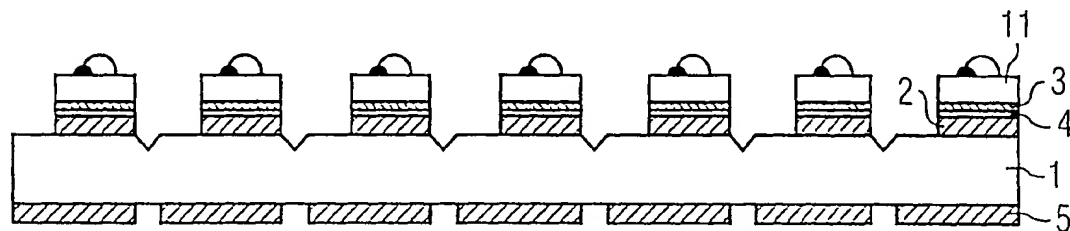
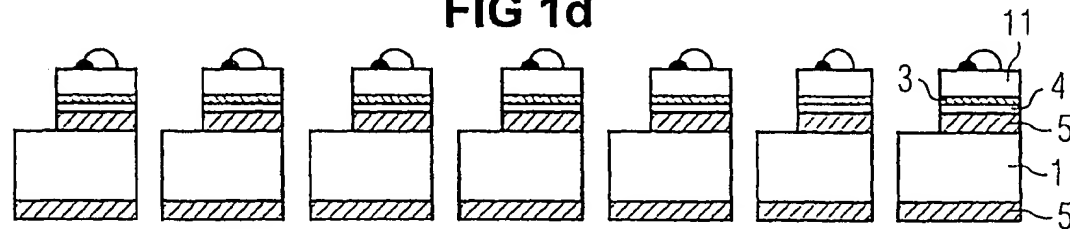


FIG 1d



*ports*  
*chip mounting area*  
*ceramic carrier*  
*cooling element*  
*metal surface*

FIG 1a

*chip*  
*4/5*

*62*  
*64*  
*83*  
*85 plate*  
*87 cooling*  
*86 metal*